

Abstract of the Disclosure

In a multi-node system, a method and apparatus to implement a request such as a purge TLB entry request is described. In one embodiment, a processor initiates a purge TLB request and any other processors assert a signal in response (pending completion of the request). A node controller coupled to the processor via a bus asserts the same signal to indicate that the request has not been completed. The node controller can then send the request to other node controller (potentially via a switching agent), so that other processors in the multi-node system can complete the request. Once all processors in the other nodes have completed the request, the node controller can deassert the signal, which indicates to the requesting processor that the request has been completed at all processor outside of its node.

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